REGISTRATION FORM

Self Financed
Short Term Training Programme on

Logic Building using C and MATLAB

January 13-17, 2016

Organized by
Department of Information Technology
and
Department of Electrical Engineering
National Institute of Technology, Raipur, C.G.

Full Name (Block Letter): ………………………………
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Name of College/Institution/Organization:
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Qualification: ……………………………………………
Designation: …………..…………………………………
Address:
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Pin code:
Phone No.:
Mobile No.:
E-mail:
Do you need accommodation: (Yes/ No):

I have enclosed the registration fee of Rs…………………by D.D. No ………………
Dated………………in favour of **"Director, NIT, Raipur"** payable at **SBI, GCET Branch, Raipur.**
Place:
Date:

Signature of the participant
Duly filled registration form must reach to Department of Electrical Engineering, NIT-Raipur before last date of receipt of registration form by post/by hand along with registration fee (D.D)

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Director, NIT Raipur

Co-Patron
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Dean (R&C), NIT Raipur

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Dr. V. P. Singh, Asstt. Prof., NIT Raipur

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Dr. G. P. Gupta, NIT Raipur.

Organized by

Department of Information Technology
and
Department of Electrical Engineering,

National Institute of Technology,
Raipur – 492010, Chhattisgarh.

About Institute

National Institute of Technology, Raipur (Formerly Government Engineering College Raipur), situated in the capital of a newly incepted state of Chhattisgarh, has proven to be avant-grade in the field of science and technology over past few decades in this region. With sweet memory of foundation ceremony by our president Hon'ble Dr. Rajendra Prasad on 14th September 1956. The institute started with two departments namely Metallurgical and Mining Engineering. Later the inauguration of the institute building was done by our Prime Minister Hon'ble Pt. Jawahar Lal Nehru on 14th March 1963. From 1st December 2005, the institute has become the National Institute of Technology. It is well connected with Mumbai, Delhi, Hyderabad, Kolkata, Bhopal, Ahmedabad, Bangalore, Chennai and Chandigarh by regular flights and railway routes.

About Departments

The **Department of Information Technology** came into existence in 2000. The department is the study, research and work place of 300 people from 28 states across India. About 15 professors in this department teach mainly in areas like Information Science & Technology, Computer Science & engineering, system-oriented sciences, and mathematics, etc., carry out research in their respective fields.

The **Department of Electrical Engineering** came into existence in 1958. The department aims at imparting the state of the art knowledge and skills to the students. The department provides an outstanding research environment and offers academic program leading to the award of B.Tech, M.Tech. and Ph.D. degree.

Introduction

The short term training program is aimed at imparting knowledge and training on logic building using C and MATLAB and their applications in various fields of engineering. Expert lectures will be delivered by faculty from academia. This program will be beneficial to UG/PG students, young researchers, research scholars, faculty who intend to practice/begin research and employ logic and programming in the field of electrical engineering, mechanical engineering, electronics engineering, computer science engineering, information technology, etc. This short term training program is proposed to give a thorough exposure of logic building using C and MATLAB. Distinguishing features of this program include training sessions on logic building using C and MATLAB.

Objective

The objective of this programme is to train the students, research scholars, and faculty of various fields. The programme aims to build the logics to handle the problems encountered in simulation of engineering and science applications. The program will also provide a platform for interaction and exchange of ideas logic building using C and MATLAB.

Eligibility

Participation in this short term training programme (STTP) is open to the under-graduate students, post-graduate students, faculty members, engineers, researchers, and other executives working on diverse fields in electronics engineering, computer engineering, electrical engineering, instrumentation engineering, biomedical engineering, biotechnology, civil engineering, mechanical engineering, etc.

Selection

Since the number of seats is limited to 40, the selection will be made on first cum first basis. However, the preference will be given to the outside participants. Intimation regarding selection will be sent to the candidates by email as per the schedule. Certificates will be issued to the participants only after attending the complete course. No TA/DA will be paid to the participants.

Accommodation may be provided on availability on payment basis at institute guest house and relevant hostels of NIT, Raipur provided request for the same is made in the registration form.

Course Contents

The STTP will be highly beneficial to the students who are just starting their research and using the platform of C, MATLAB, etc.

The contents of the STTP are as follows:
Introduction to logic building.
Handling control constructs.
Data types and operators.
Defining and manipulating arrays.
Structures and functions.
File and database handling.
Mapping the logics from C into MATLAB.
Introduction of tool boxes of MATLAB.
Design of logic/code for various engineering applications/problems.

Registration Fee

UG Students	**Rs. 1000**
PG Students	**Rs. 2000**
Research Scholars	**Rs. 5000**
Faculty/Industry Delegates	**Rs. 7000**

Note:
Fee is non-refundable.

Important Dates

**Last date of receipt of application: 30.12.2015,
Notification of selection (by Email): 05.01.2016.**

Venue

E-Class Room, Department of Information Technology, and Department of Electrical Engineering, NIT Raipur (CG) – 492010, INDIA.

Contacts and Correspondence

Contact following for any further query. Mail/post your filled in registration form to:

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