



राष्ट्रीय प्रौद्योगिकी संस्थान रायपुर
NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR
(Institute of National Importance)
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No./NITRR/Electrical/Project/2018/

Raipur, Dated: 25.06.2018

Applications are invited for the post of one Junior Research Fellow (JRF) for a period of three years (reviewed annually) as per the following requirements, eligible candidates can attend the Walk-in interview on **01/08/2018 at 10 : 30 AM** in Department of Electrical Engineering along with the completed application form and necessary original certificates.

Name of the Position	Junior Research Fellow (JRF)
Project Title	Design and Development of Low Cost Single-Phase Fault Tolerant Multilevel Inverter Topology
Principal Investigator and Co- Principal Investigator	Dr. Shubhrata Gupta Professor Department of Electrical Engineering, NIT Raipur Dr. Lalit Kumar Assistant Professor Department of Electrical Engineering, NIT Raipur
Funding Agency	DST-SERB under EMR Scheme, Project No. EMR/2017/000447
Tenure of Scheme	Three Years and performance will be reviewed on yearly basis for continuation
Fellowship (Consolidated)	Rs. 25,000/- (fixed) per month for the First 24 month and Rs. 28,000/- per month for remaining 12 months. HRA will be given as per institute norms.
Essential Qualifications	BE/B.Tech with NET, GATE Qualification or M.Tech/M.E/MS in Electrical Engineering, Electrical and Electronics Engineering or relevant discipline
Desirable Qualification	Prior research/work experience on Power Electronics Converters, Digital Controller, Good Programming skill and Knowledge of MATLAB/ Simulink
Date and Time of Interview	01/08/2018 , 10:30 AM
Venue	Department of Electrical Engineering, NIT Raipur

Note : Selected eligible candidate will be encouraged to apply for the Ph.D. program in the Institute

Terms and Conditions:

1. The above position is purely temporary and continuation of the fellowship is subject to satisfactory performance and review after each year.
2. Candidates are required to appear for the interview with photocopy of the application form, a brief bio-data, and all relevant original certificates with a set of self-attested photocopies of the certificates with two passport photographs.
3. No TA/DA is admissible for appearing in interview.
4. Selected candidates will not be permitted to claim for any regular/part time appointments in this institute or any other institutes during this period.
5. Canvassing in any form will disqualify the candidature.
6. The appointment shall be co-terminus with the project.

For further details please contact

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DEPARTMENT OF ELECTRICAL ENGINEERING

APPLICATION FOR THE POST OF JUNIOR RESEARCH FELLOW (JRF)

ADVT. NO..... DATED.....

POST APPLIED FOR.....

TITLE OF PROJECT.....

Attach Passport
Size Self-attested
Photograph

1. NAME OF CANDIDATE (in Block letter):
2. DOB:
3. AGE AS ON CLOSING DATE OF THE APPLICATION: YEAR/MONTH/DAYS:
4. ADDRESS (with contact number & e-mail):
5. FATHER'S NAME:
6. SC/ST/OBC(NON-CREAMY LAYER)/GENERAL:
7. QUALIFICATION (High School onwards) (Attach Certificates):

Sl. No	Name of Exam	Board or University	% Marks	Year	Grade or Division	Remarks

8. WORK EXPERIENCE (If any) (Attach Certificates):
9. SKILL :
10. PUBLICATION (If any) (Attach a copy):

UNDERTAKING

I understand that it is a contractual engagement on purely temporary basis for the Project. I declare that the above particulars are correct to the best of my knowledge and belief. I understand that action can be taken against me if these are found to be incorrect.

Place:

Date:

SIGNATURE OF CANDIDATE